

AMENDMENTS TO THE CLAIMS

The following listing of claims will replace all prior versions and listings of claims in the application.

LISTING OF CLAIMS

1. (previously presented) A surface mounting chip network component having an even number of network circuits formed on the surface of rectangular insulating substrate, each of the network circuits having three or more odd number of terminals, comprising an equal number of said terminals to be arranged each of the facing sides on said insulating substrate, and elements of said network circuits to be formed on both faces of said insulating substrate.
2. (previously presented) A surface mounting chip network component having an even number of network circuits formed on the surface of rectangular insulating substrate, each of the network circuits having three or more odd number of terminals, comprising said terminals to be arranged on all of four sides, the number of terminals on a side being equal to one on the opposite side and the number of terminals on one of the remaining sides being equal to one on the last side, and elements of said network circuits to be formed on both faces of said insulating substrate.
3. (currently amended) A surface mounting chip network component according to claim 1 ~~or 2~~, wherein individual network circuit has common conductor extended from a terminal on a side of said insulating substrate, and one or more elements selected from:

- (a) a resistor connected to said common conductor at an edge;
- (b) another terminal on a side of said insulating substrate to which the other edge of said resistor is connected;
- (c) another resistor formed on the reverse side with respect to the face of the insulating substrate where the resistor of (a) is formed, an edge of the resistor to be connected to the terminal of (b); and
- (d) an independent terminal to be connected to the other edge of the resistor of (c), the terminal to be located on the opposite side with respect to the side of said insulating substrate where the terminal of (b) is arranged.

4. (currently amended) A surface mounting chip network component according to claim 1, ~~2 or 3~~, wherein all of said network circuits formed on said insulating substrate are equivalent and the terminals of the corresponding two network circuits are arranged point-symmetrically with respect to the center of the surface of said insulating substrate.

5. (currently amended) A surface mounting chip network component according to claim 1, ~~2, 3, or 4~~, wherein said insulating substrate has a means to discriminate face.

6. (currently amended) A surface mounting chip network component according to claim 1, ~~2, 3, 4 or 5~~, wherein said means is different colors of overcoat for network circuits on both faces of said insulating substrate or signs printed on the surface of the overcoat.

7. (new) A surface mounting chip network component according to claim 2, wherein individual network circuit has common conductor extended from a terminal on a side of said insulating substrate, and one or more elements selected from:

- (a) a resistor connected to said common conductor at an edge;
- (b) another terminal on a side of said insulating substrate to which the other edge of said resistor is connected;
- (c) another resistor formed on the reverse side with respect to the face of the insulating substrate where the resistor of (a) is formed, an edge of the resistor to be connected to the terminal of (b); and
- (d) an independent terminal to be connected to the other edge of the resistor of (c), the terminal to be located on the opposite side with respect to the side of said insulating substrate where the terminal of (b) is arranged.

8. (new) A surface mounting chip network component according to claim 2, wherein individual network circuit has common conductor extended from a terminal on a side of said insulating substrate, and one or more elements selected from:

- (a) a resistor connected to said common conductor at an edge;
- (b) another terminal on a side of said insulating substrate to which the other edge of said resistor is connected;
- (c) another resistor formed on the reverse side with respect to the face of the insulating substrate where the resistor of (a) is formed, an edge of the resistor to be connected to the terminal of (b); and
- (d) an independent terminal to be connected to the other edge of the resistor of (c), the terminal to be located on the opposite side with respect to the side of said insulating substrate where the terminal of (b) is arranged.

9. (new) A surface mounting chip network component according to claim 2, wherein said insulating substrate has a means to discriminate face.

10. (new) A surface mounting chip network component according to claim 2, wherein said means is different colors of overcoat for network circuits on both faces of said insulating substrate or signs printed on the surface of the overcoat.